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Generated by: Genus(TM) Synthesis Solution 17.22-s017\_1

Generated on: Jul 25 2024 09:35:23 am

Module: voting\_machine

Operating conditions: slow (balanced\_tree)

Wireload mode: enclosed

Area mode: timing library

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Gate Instances Area Library

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AND2X1 2 26.611 tsmc18

AND2X2 3 39.917 tsmc18

AND2X4 7 116.424 tsmc18

AND3X2 2 33.264 tsmc18

AOI22X1 72 1197.504 tsmc18

AOI2BB1XL 2 33.264 tsmc18

CLKBUFX2 1 13.306 tsmc18

CLKBUFX8 1 23.285 tsmc18

CLKINVX2 1 9.979 tsmc18

CLKINVX3 18 179.626 tsmc18

CLKINVX4 7 93.139 tsmc18

CLKINVX8 4 79.834 tsmc18

DFFRHQX1 13 908.107 tsmc18

DFFRHQX2 10 864.864 tsmc18

DFFRHQX4 38 4044.902 tsmc18

DFFRHQXL 16 1117.670 tsmc18

DFFRX1 2 153.014 tsmc18

DFFRX4 20 1995.840 tsmc18

INVX1 21 139.709 tsmc18

INVX2 5 49.896 tsmc18

INVX4 4 53.222 tsmc18

INVX8 4 79.834 tsmc18

INVXL 80 532.224 tsmc18

JKFFRX4 2 232.848 tsmc18

MXI2X1 3 69.854 tsmc18

NAND2BX1 3 39.917 tsmc18

NAND2X1 48 479.002 tsmc18

NAND2X2 7 116.424 tsmc18

NAND2X4 8 186.278 tsmc18

NAND2XL 14 139.709 tsmc18

NAND3BX1 2 33.264 tsmc18

NAND3BXL 1 16.632 tsmc18

NAND3X1 10 133.056 tsmc18

NAND3XL 17 226.195 tsmc18

NAND4BX1 2 46.570 tsmc18

NAND4BX2 1 33.264 tsmc18

NAND4X1 5 83.160 tsmc18

NAND4XL 5 83.160 tsmc18

NOR2BX4 2 53.222 tsmc18

NOR2X1 12 119.750 tsmc18

NOR2X2 9 149.688 tsmc18

NOR2X4 5 116.424 tsmc18

NOR2XL 1 9.979 tsmc18

NOR3BX2 1 26.611 tsmc18

NOR3X1 6 79.834 tsmc18

NOR3XL 1 13.306 tsmc18

NOR4BX1 3 59.875 tsmc18

OAI21XL 1 13.306 tsmc18

OAI2BB2X1 66 1536.797 tsmc18

OAI2BB2XL 22 512.266 tsmc18

OAI32X1 1 23.285 tsmc18

OR2X2 1 13.306 tsmc18

SDFFRHQX1 96 7983.360 tsmc18

TLATX1 4 146.362 tsmc18

XOR2X1 22 585.446 tsmc18

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total 714 25147.584

Type Instances Area Area %

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sequential 201 17446.968 69.4

inverter 144 1217.462 4.8

buffer 2 36.590 0.1

logic 367 6446.563 25.6

physical\_cells 0 0.000 0.0

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total 714 25147.584 100.0